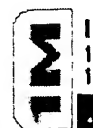


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Circuits and Systems, 1996. ISCAS '96., 'Connecting the World', 1996 IEEE International Symposium on , Volume: 4 , 12-15 May 1996

Pages:77 - 80 vol.4

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Electrical and Computer Engineering, 2002. IEEE CCECE 2002. Canadian Conference on , Volume: 2 , 12-15 May 2002

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L1	3845	filter adj processing	USPAT; DERWENT	OR	ON	2005/01/10 09:28
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L3	7	2 and (low adj frequency) and (high adj frequency)	USPAT; DERWENT	OR	ON	2005/01/10 09:29
L4	0	3 and DWT	USPAT; DERWENT	OR	ON	2005/01/10 09:29
L5	0	2 and DWT	USPAT; DERWENT	OR	ON	2005/01/10 09:29
L6	2	1 and DWT	USPAT; DERWENT	OR	ON	2005/01/10 09:29
L7	1	6 and rotation	USPAT; DERWENT	OR	ON	2005/01/10 09:29
L8	1	6 and rotat\$4	USPAT; DERWENT	OR	ON	2005/01/10 09:30
L9	242	DWT and filter	USPAT; DERWENT	OR	ON	2005/01/10 09:30
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L12	435	382/240.ccls.	USPAT; DERWENT	OR	ON	2005/01/10 09:32
L13	253	12 and filter	USPAT; DERWENT	OR	ON	2005/01/10 09:32
L14	42	13 and DWT	USPAT; DERWENT	OR	ON	2005/01/10 09:32
L15	24	14 and vertical and horizontal	USPAT; DERWENT	OR	ON	2005/01/10 09:32
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L18	0	15 and (rotat\$4 with 2x2)	USPAT; DERWENT	OR	ON	2005/01/10 09:40
L19	0	14 and (rotat\$4 with 2x2)	USPAT; DERWENT	OR	ON	2005/01/10 09:41
L20	0	13 and (rotat\$4 with 2x2)	USPAT; DERWENT	OR	ON	2005/01/10 09:41
L21	2	filter and DWT	EPO; JPO	OR	ON	2005/01/10 09:43
L22	9834	filter and (high adj frequency)	EPO; JPO	OR	ON	2005/01/10 09:43
L23	1843	22 and (low adj frequency)	EPO; JPO	OR	ON	2005/01/10 09:44

L24	0	23 and (rotation near 2x2)	EPO; JPO	OR	ON	2005/01/10 09:44
L25	0	23 and (rotation near "90")	EPO; JPO	OR	ON	2005/01/10 09:44
L26	0	23 and (rotation near "90")	EPO; JPO	OR	ON	2005/01/10 09:44
L27	30	23 and rotation	EPO; JPO	OR	ON	2005/01/10 09:44
L28	1	27 and "90" adj degrees	EPO; JPO	OR	ON	2005/01/10 09:45

	U	1	Document ID	Issue Date	Page s	Title
1			US 6385329 B1	20020507	46	Wavelet domain watermarks
2			US 2002016811 3 A	20021114	48	Filter processing apparatus includes rotation unit which rearranges data output from processor by rotating data at specified angle

	Current OR	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3
1	382/100			Sharma; Ravi K. et al.	X				
2				NAKAYAMA, T	X				

	4	5	Image Doc. Displayed	PT
1			US 6385329	
2			US 20020168113	

	U	1	Document ID	Issue Date	Page s	Title
1			US 6798917 B2	20040928	29	Image input apparatus, image processing apparatus, image input method, image processing method and image input system
2			US 6442298 B1	20020827	20	Transformation and selective inverse transformation of large digital images
3			US 6201897 B1	20010313	20	Transformation and selective inverse transformation of large digital images
4			US 5737448 A	19980407	19	Method and apparatus for low bit rate image compression



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1	382/240	382/239; 382/251		Fujiwara; Hideyuki et al.	X				
2	382/248	382/240		Nixon; Stuart William	X				
3	382/248	382/240		Nixon; Stuart William	X				
4	382/240	348/441		Gardos; Thomas R.	X				

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